**Meeting Minutes**

**Date:** March 20, 2014

**Start Time:** 6:00pm

**End Time:** 6:45pm

**Members Present:** Drew Aaron, Michael Beaver, Clay Borden, Chad Farley,

Dr. Patricia Roden, Andrew Hamilton, and Travis Hunt

**Members Absent:** N/A

**Topics** **Discussed**

* Client Questions #5
* Progress Report

**Decisions and Actions Taken**

The team met with the client and discussed the fifth set of client questions. See the attached responses by the client.

Travis walked the client through the team’s progress report and projected schedule, including work to be conducted during Spring Break. The team’s approach to the Machine Op Table is totally inefficient for a large instruction set, but it is adequate for the current subset. Some of the milestones are rather ambitious. The Symbol Table and the Literal Table must be finished so Pass 1 of the Assembler can be completed. To quote the client, “That is *so* ambitious.”

**Supplementary Information**

**Client Responses**

1. How exactly do we construct the Program Status Word?

**Drew Aaron:** PSW is set according to the machine state. Building it is beyond our scope. Just worry about CC and instruction address.

**Michael Beaver:** The PSW is set according to the state of the machine. The CC and the instruction address are sufficient to show; mask the rest of the PSW. The CC should be shown in bits, and the instruction address should be shown in six hex digits. Note that the instruction address is the address of the *next* instruction.

**Clay Boren:** Set by different parts of the assembler. Set according to the status of the machine. Just show condition code and the location counter.

**Chad Farley:** PSW is set by “status” of machine. Beyond our scope. Do not concern (mask all except CC and LOC). The CC should be in binary and the LOC in hex. Mask unnecessary parts.

**Andrew Hamilton:** Wiki page. Just need CC and instruction address.

**Travis Hunt:** See Wikipedia page (IBM\_System/360\_architecture). Made up of several parts. Only have condition code (binary) and location counter (hex). Grey out the rest of the PSW. The location counter points to the next instruction.

2. For the instruction trace of a crash report, would a maximum of 10 previous instructions before the crash be sufficient?

**DA:** Yes.

**MB:** Yes.

**CB:** Yes.

**CF:** 10 is sufficient.

**AH:** Yes.

**TH:** Yes. 10 is sufficient.

3. For the instruction trace of a crash report, what is the IM column? What does this column specify?

**DA:** Don’t do IM.

**MB:** No IM column.

**CB:** Do not do IM column.

**CF:** Not needed for our project.

**AH:** Don’t do.

**TH:** Do not have to worry about it.

4. If you no longer require line numbers alongside the text editor, do you still require column headings atop the text editor?

**DA:** Nope. Remove it.

**MB:** Take it out.

**CB:** Take out column headings.

**CF:** Take out the column ruler.

**AH:** Take out.

**TH:** Take out.

5. Where would you like the dynamic line number and column number indicators to be located? Note that they are currently located in the bottom right corner of the main window.

**DA:** Top left.

**MB:** Move to the top left like ASSIST/I.

**CB:** Top left (like ASSIST/I).

**CF:** Mimic ASSIST/I (top).

**AH:** Top left.

**TH:** Top left corner.

6. Which specific code and syntactical elements need to be highlighted?

**DA:** Comments only.

**MB:** Comments (both types) *only*.

**CB:** Comments (both types).

**CF:** Comments, and only comments.

**AH:** Comments—both types.

**TH:** Comments (both types).

7. How many different syntax highlighting colors are required?

**DA:** Only comments and use deep pink default and have a color picker.

**MB:** Deep pink with option to select custom color.

**CB:** Just comments.

**CF:** Just one for comments.

**AH:** Deep pink with a color picker.

**TH:** One.

8. Should *all* instructions be one color, or should each type of instruction (e.g., RX and RR) have its own color?

**DA:** Disregard.

**MB:** Disregard.

**CB:** Disregard.

**CF:** Disregard.

**AH:** Disregard.

**TH:** Disregard.

9. What is the object code for the XREAD instruction?

**DA:** See email from the client.

**MB:** See email from the client.

**CB:** Will be sent over email.

**CF:** Will be sent over email.

**AH:** Will be sent over email.

**TH:** Will be sent over email.

10. What is the object code for the XPRNT instruction?

**DA:** See email from the client.

**MB:** See email from the client.

**CB:** Will be sent over email.

**CF:** Will be sent over email.

**AH:** Will be sent over email.

**TH:** Will be sent over email.